

FIG. 1

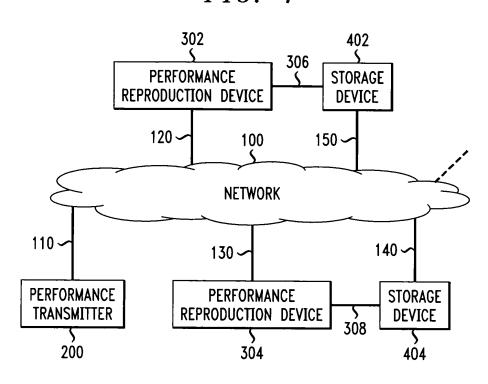
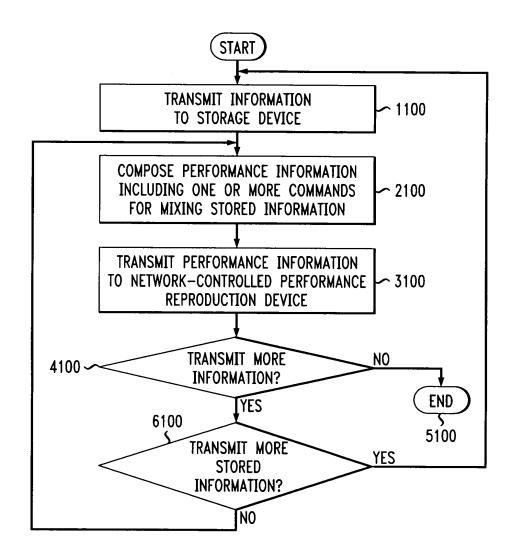


FIG. 2 200 220 230 PERFORMANCE **COMMAND** INPUT DEVICE INPUT DEVICE 260 **NETWORK** CONTROLLER **MEMORY INTERFACE** 210 250 240



FIG. 3





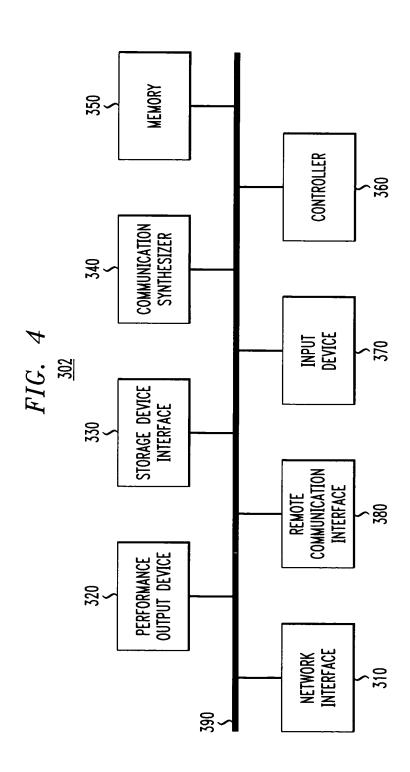
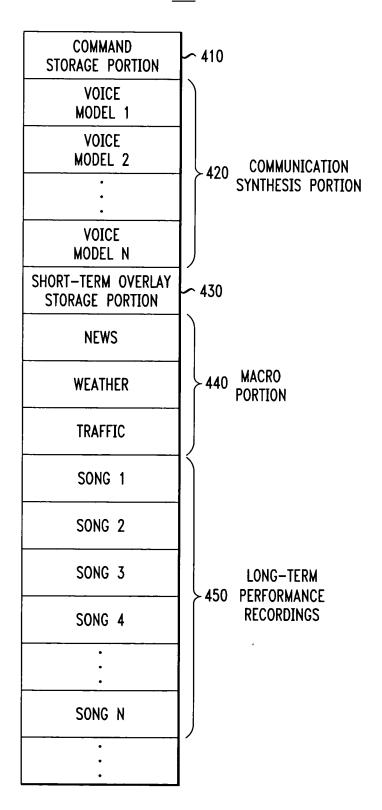
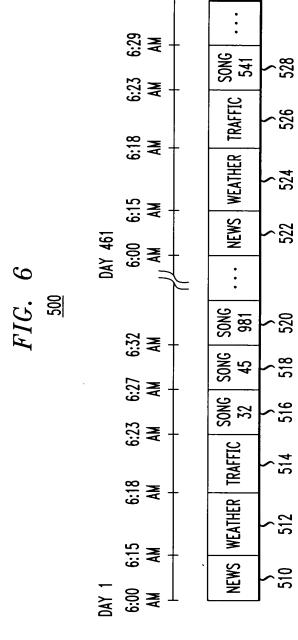




FIG. 5









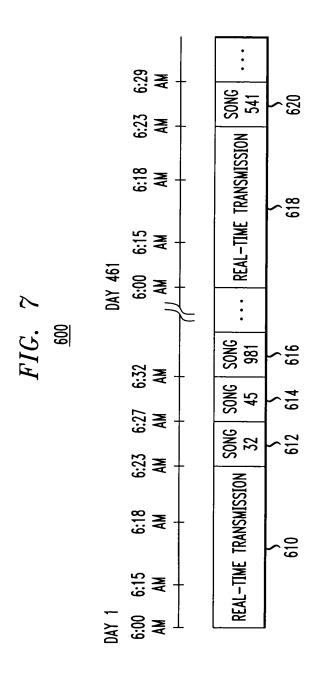




FIG. 8

710	712	714	716	718	720	722	724	
	\$		\$	\$	\$		S	
SONG 32	SONG 45		SONG 451			SONG 682	SONG 121	• • •

FIG. 9

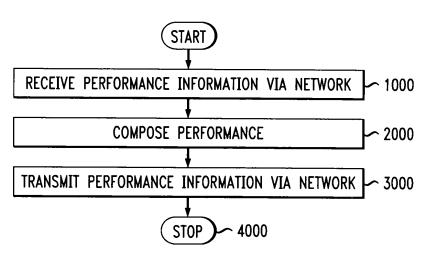




FIG. 10

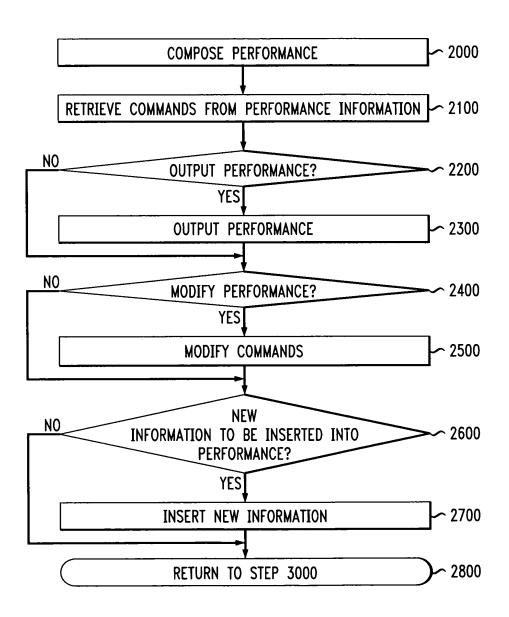




FIG. 11

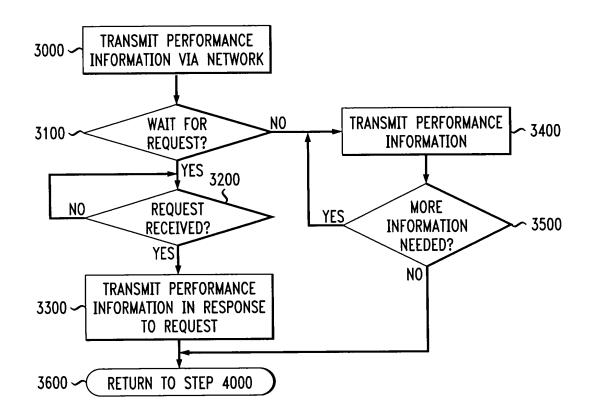




FIG. 12

